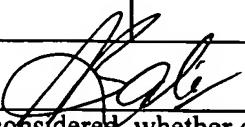


INFORMATION DISCLOSURE STATEMENT BY APPLICANT <small>FEB 20 2003 PATENT & TRADEMARK OFFICE U.S. GOVERNMENT</small>		Attorney Docket Number	6420-57079
		Application Number	10/047,980
		Filing Date	January 11, 2002
		First Named Inventor	Villalobos
		Art Unit	2858-2623
		Examiner Name	
U.S. PATENT DOCUMENTS			
Examiner's Initials*	Cite No. (optional)	Number	Date
uJ		4,655,592	4/7/1987
uJ		6,167,148	12/26/2000
uJ		6,324,298	11/27/2001
uJ		6,424,733	6/23/2002
uJ		6,454,437	9/24/2002
Examiner's Initials*	Cite No. (optional)	OTHER DOCUMENTS	
uJ		Claims from allowed patent application serial no. 09/759,792, filed 1/12/2001, entitled "METHOD AND APPARATUS FOR ILLUMINATING PROJECTING FEATURES ON THE SURFACE OF A SEMICONDUCTOR WAFER"	

EXAMINER SIGNATURE:	DATE CONSIDERED:
 2/16/05	
* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.	

INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Docket: 6420-57079	App: 10/047,980		
				Applicant: Villalobos			
				Filed: January 11, 2002	Art Unit: 2858-2623		
U.S. PATENT DOCUMENTS							
Init.*	Number	Date	Name	Class	Sub	Filed	
VB	6,098,887	8/8/00	Figarella et al.				
VB	6,084,397	7/4/00	Downes				
VB	6,075,883	6/13/00	Stern et al.				
VB	6,072,898	6/6/00	Beaty et al.				
VB	6,066,857	5/23/00	Fantone et al.				
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VB	5,118,192	6/2/92	Chen et al.				
VB	5,018,803	5/28/91	Hecker et al.				
EXAMINER: 				DATE	2/16/05		
*Examiner: Initial if considered, whether or not in conformance with MPEP 609; draw line through cite if not in conformance and not considered. Send copy.							